



Docket No.: M4065.0771/P771
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Roger Panicacci

Application No.: 09/281,358

Confirmation Number: 7144

Filed: March 30, 1999

Art Unit: 2615

For: ANALOG TO DIGITAL CONVERTER
WITH INTERNAL DATA STORAGE

Examiner: N. Tran

SUBMISSION OF FORMAL DRAWINGS

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SEP 08 2003

Technology Center 2600

Dear Sir:

Submitted herewith is one set (twenty sheets, twenty figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: September 3, 2003

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher S. Chow

Registration No.: 46,493

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

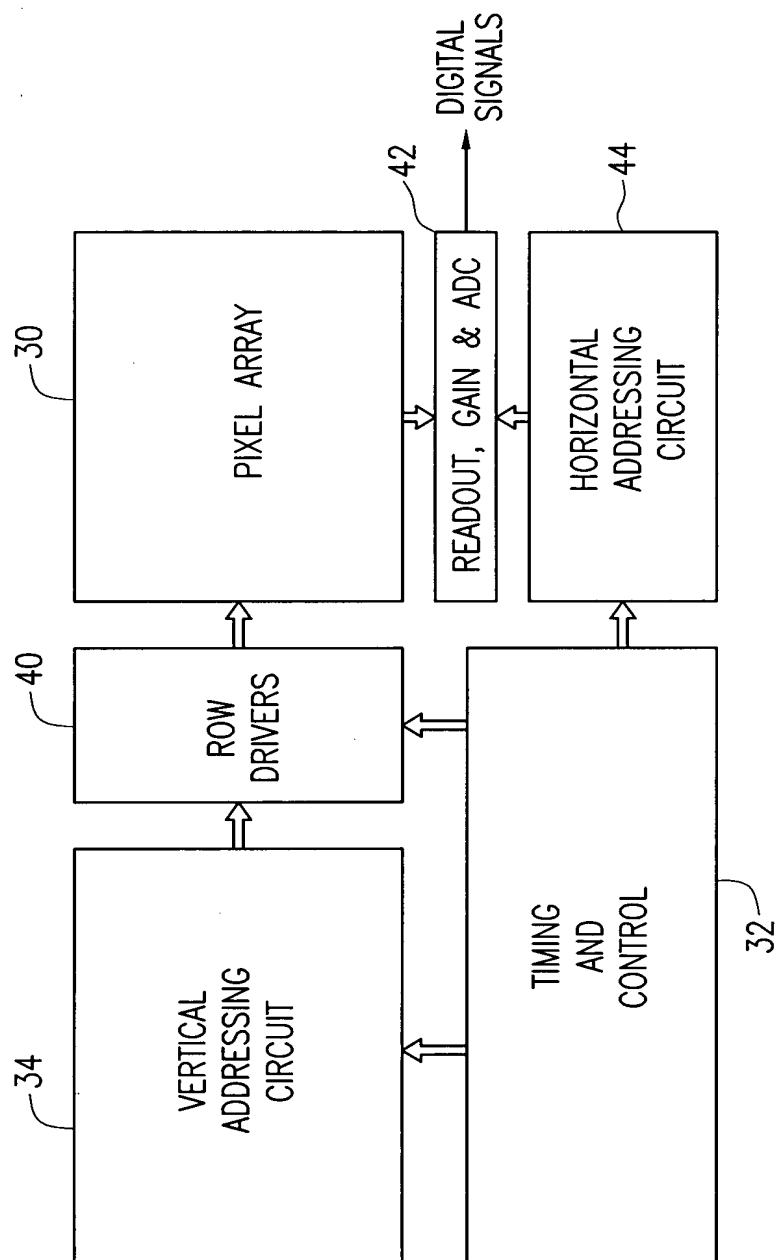


FIG. 1

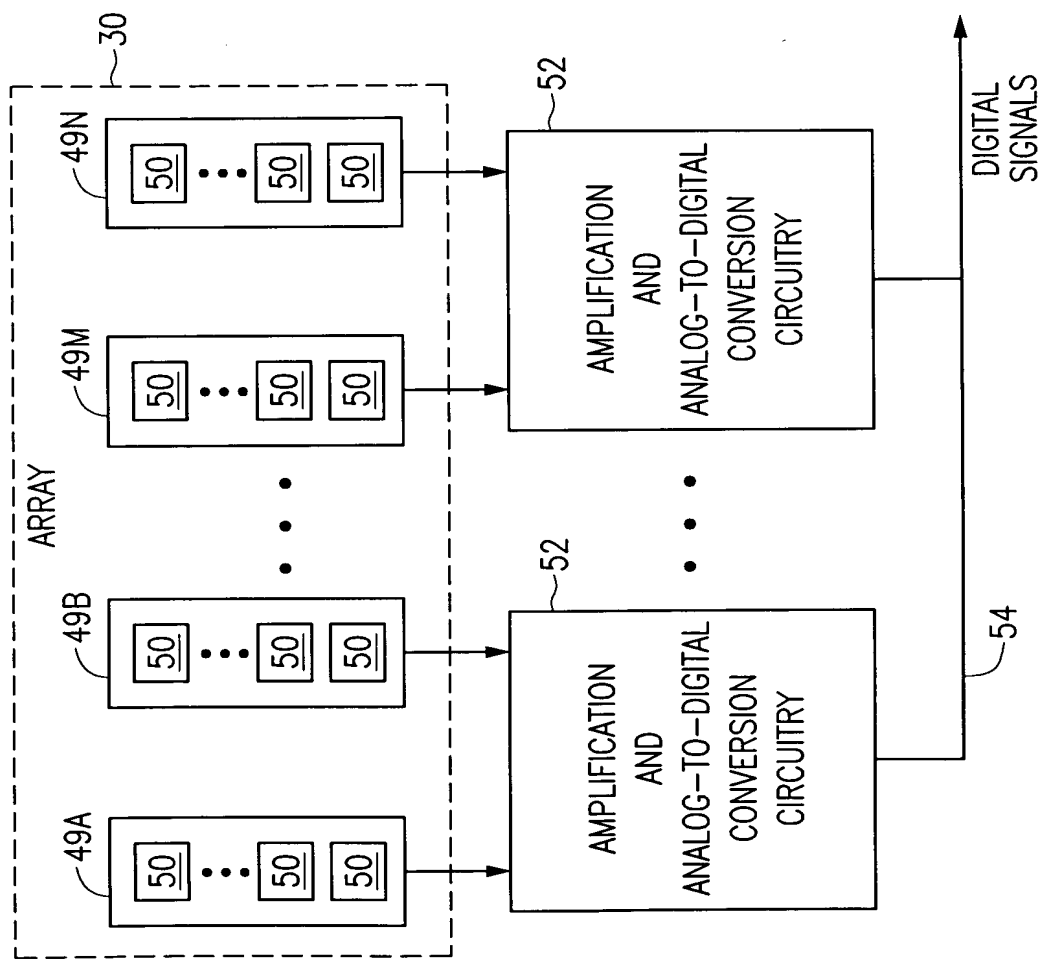
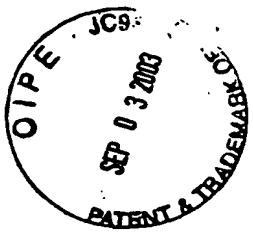


FIG. 2

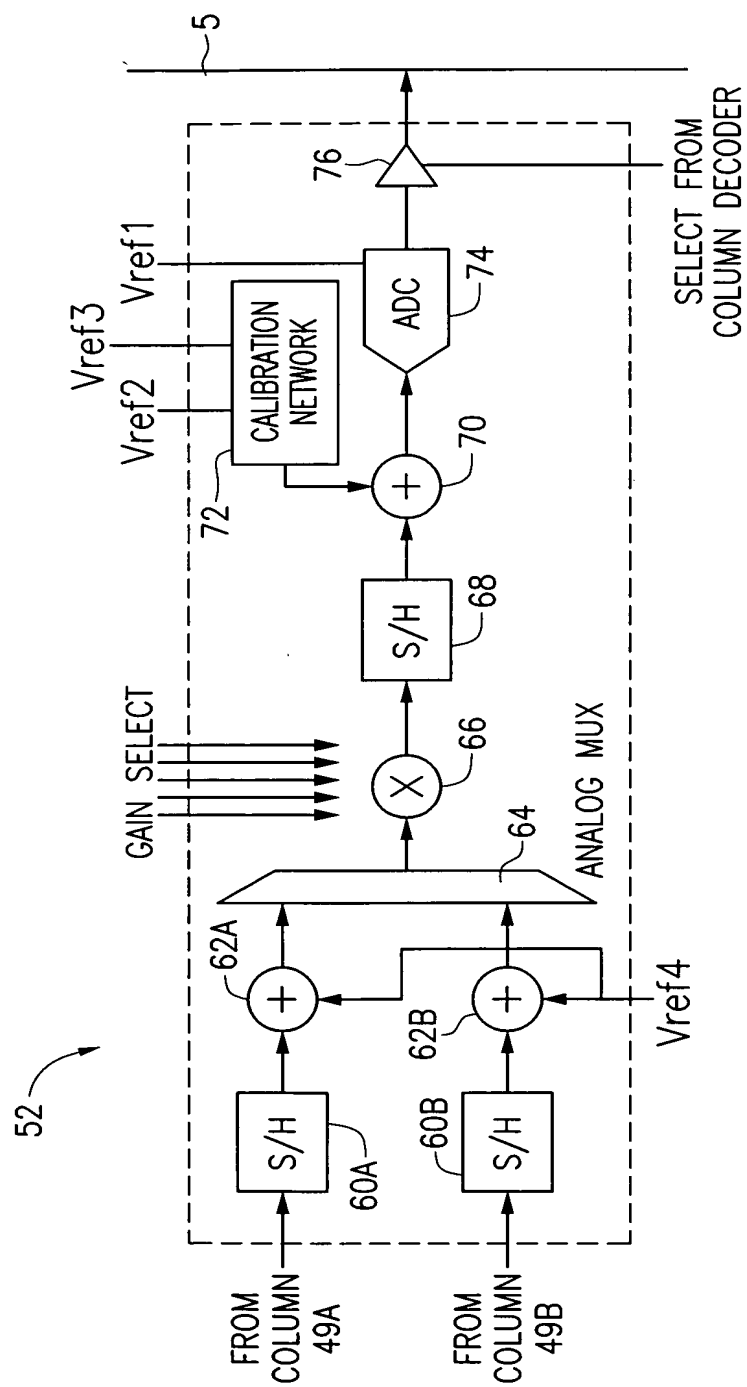


FIG. 3

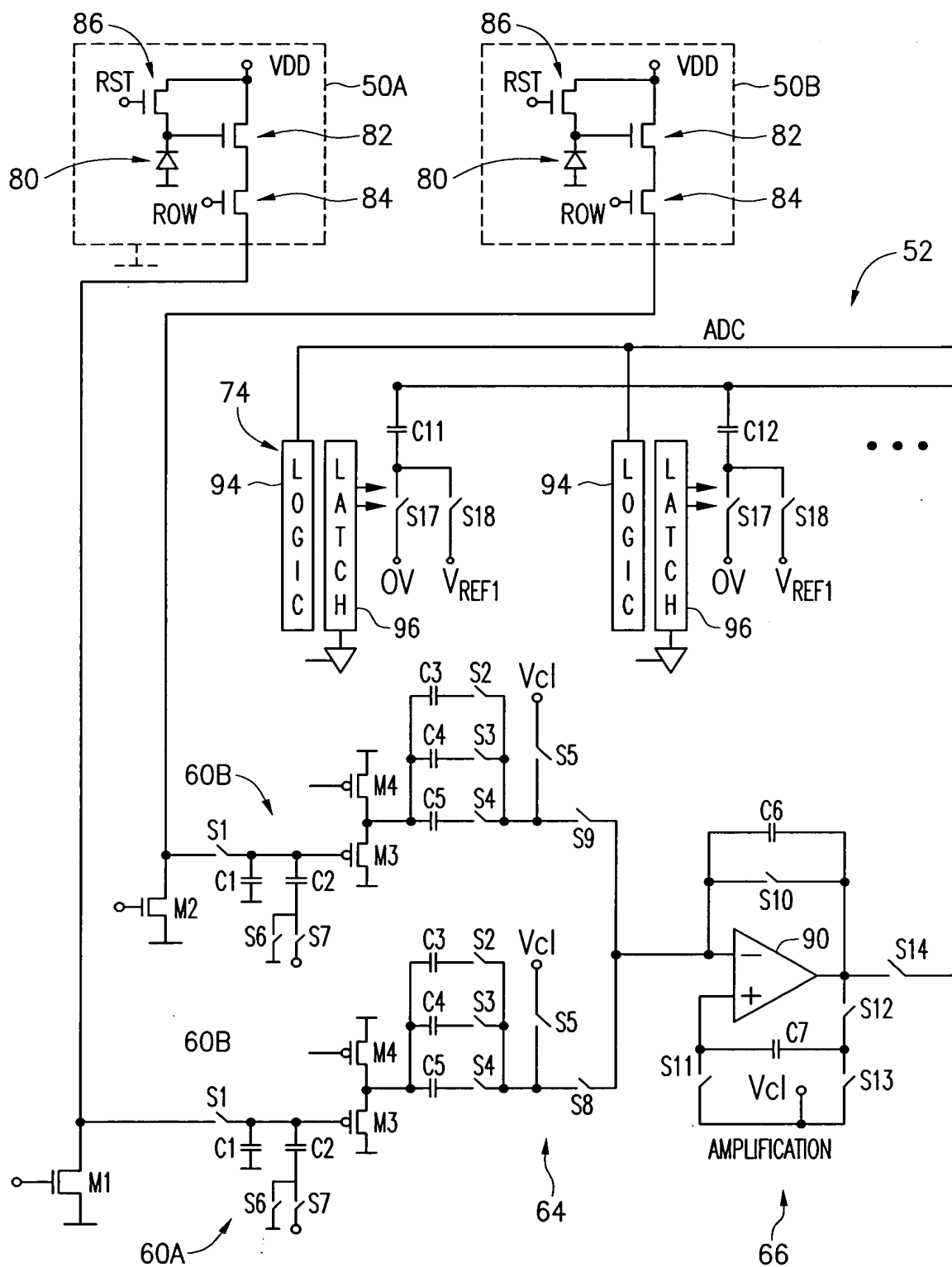


FIG. 4

FIG. 4A	FIG. 4B
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FIG. 4A

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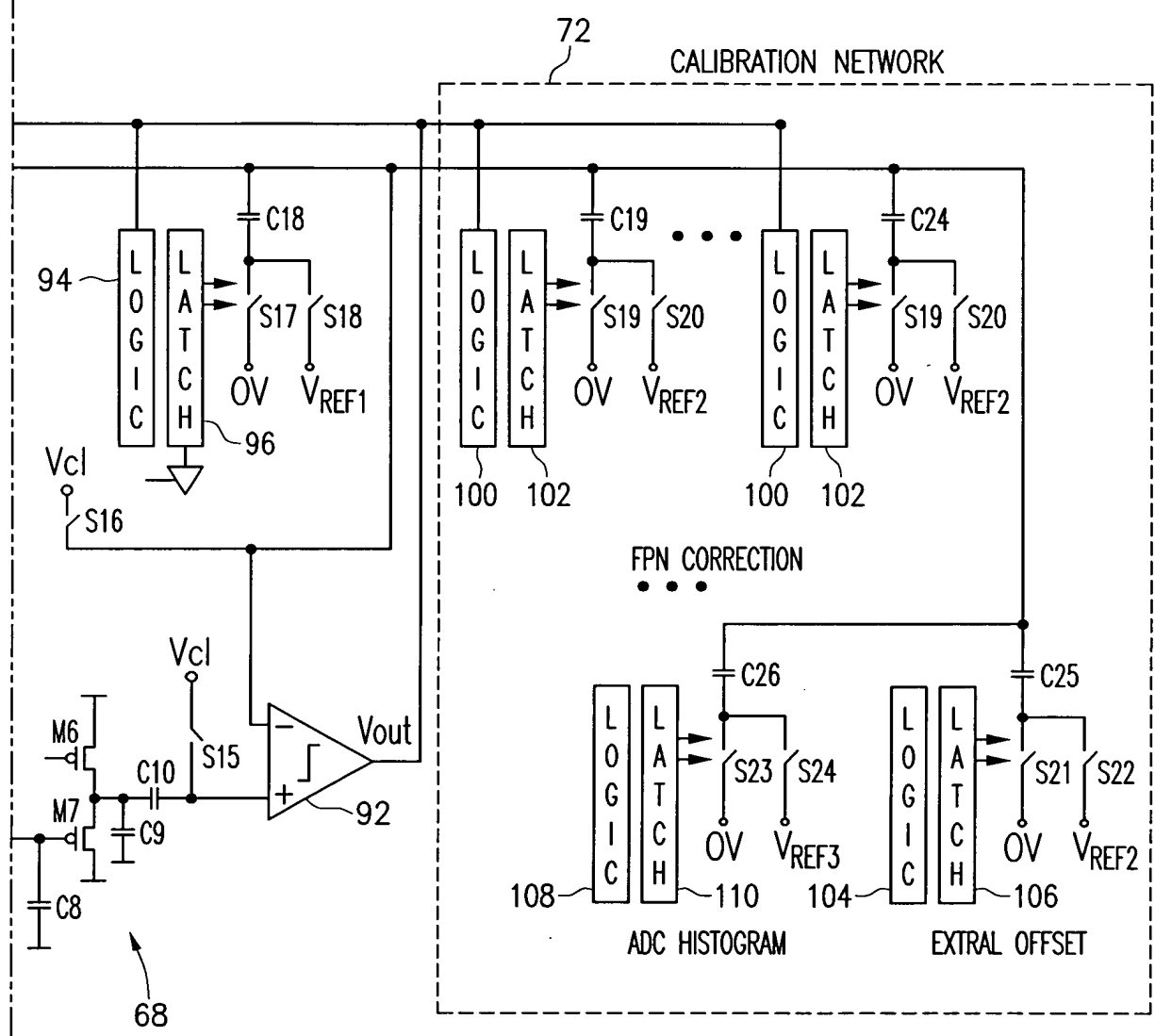


FIG. 4

FIG. 4A	FIG. 4B
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FIG. 4B

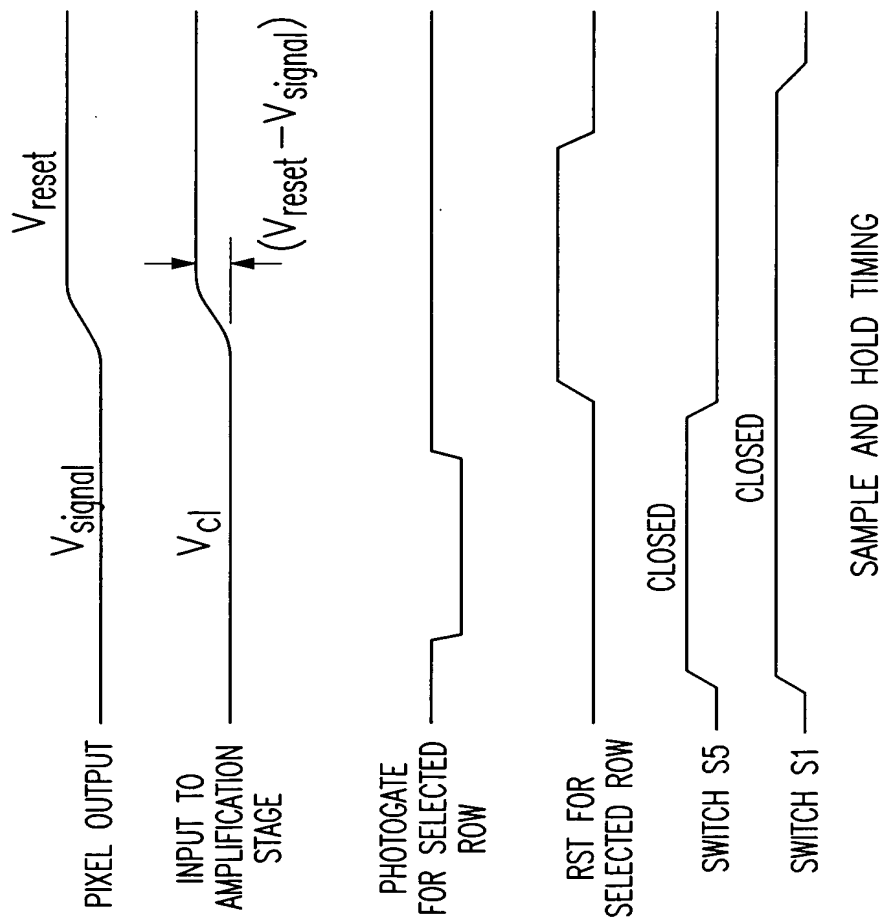
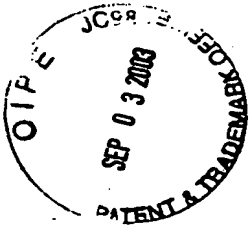


FIG. 5

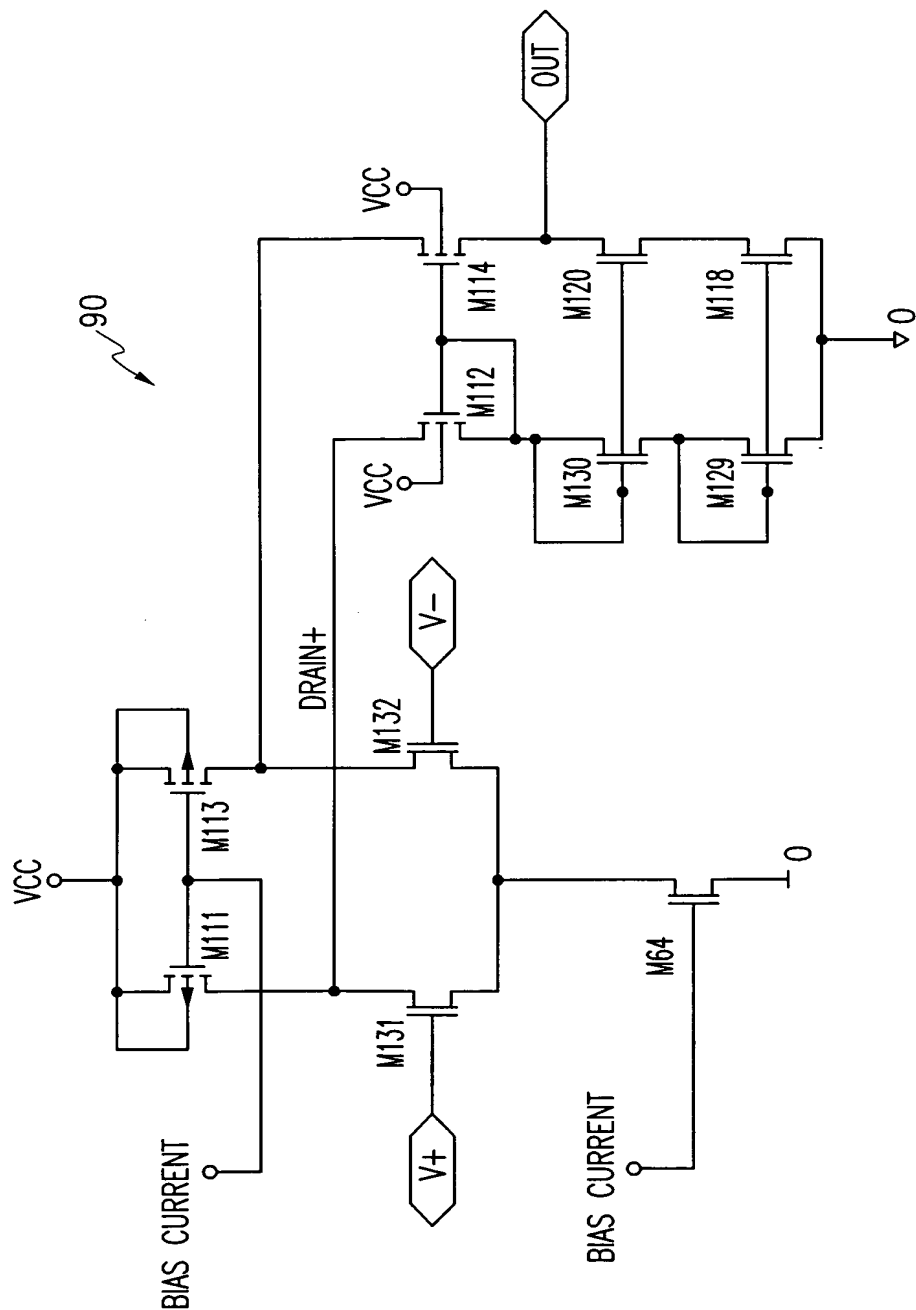
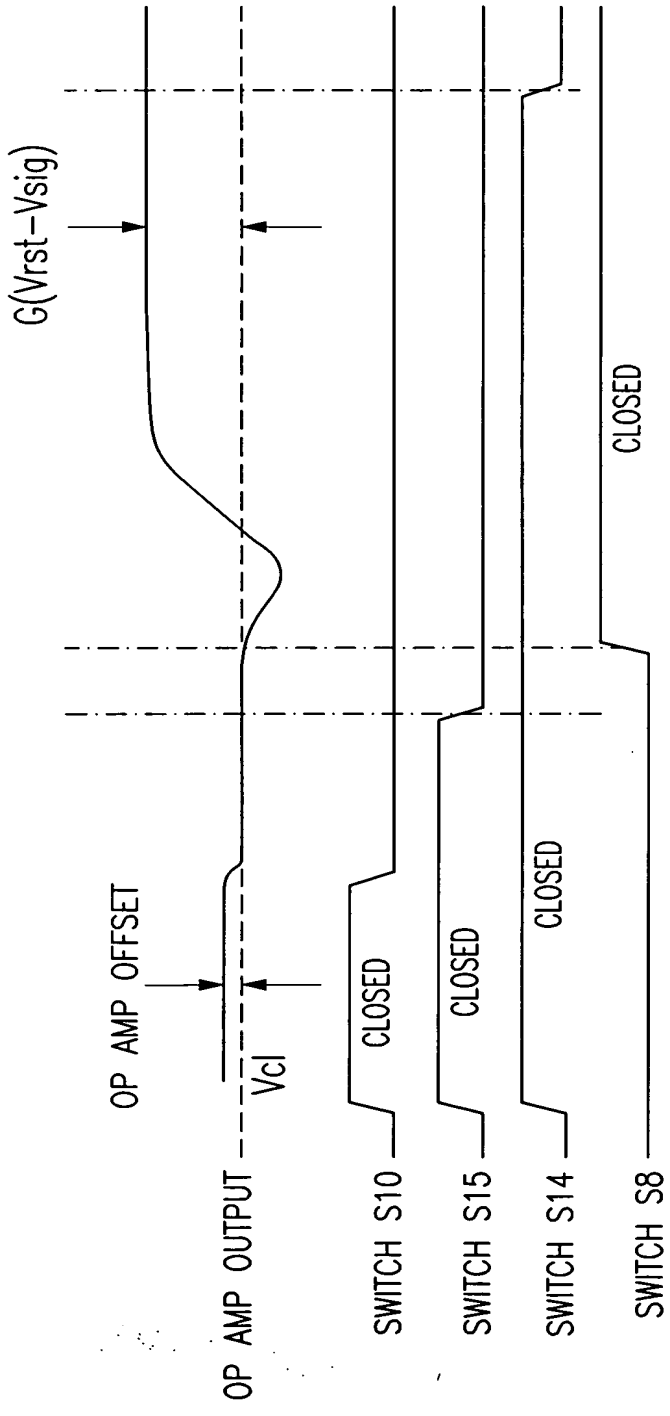
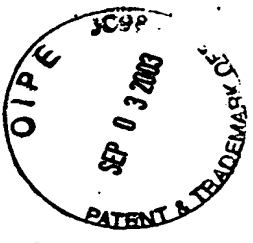


FIG. 6



AMPLIFICATION TIMING

FIG. 7

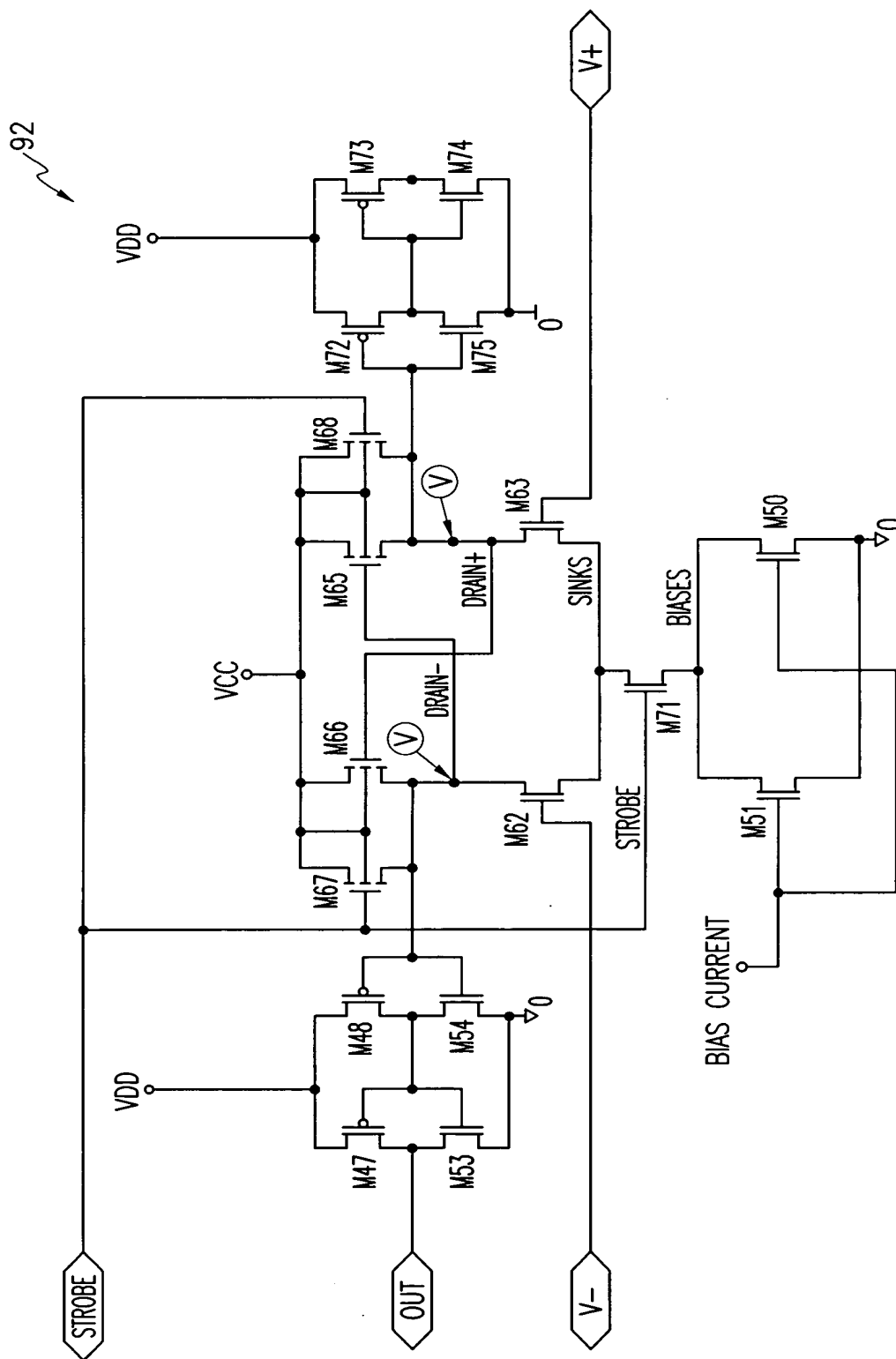


FIG. 8

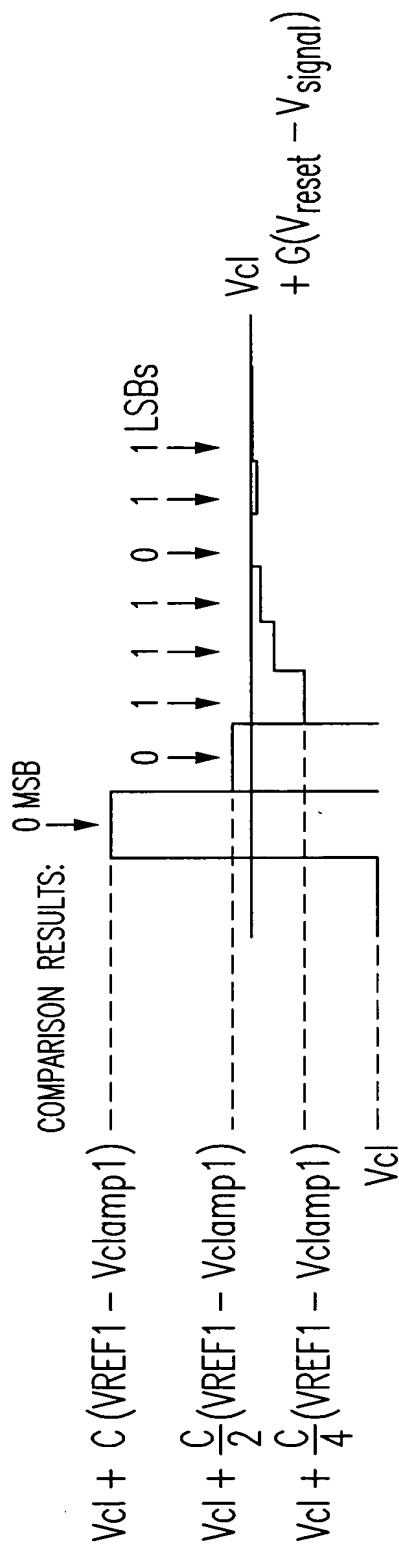


FIG. 9

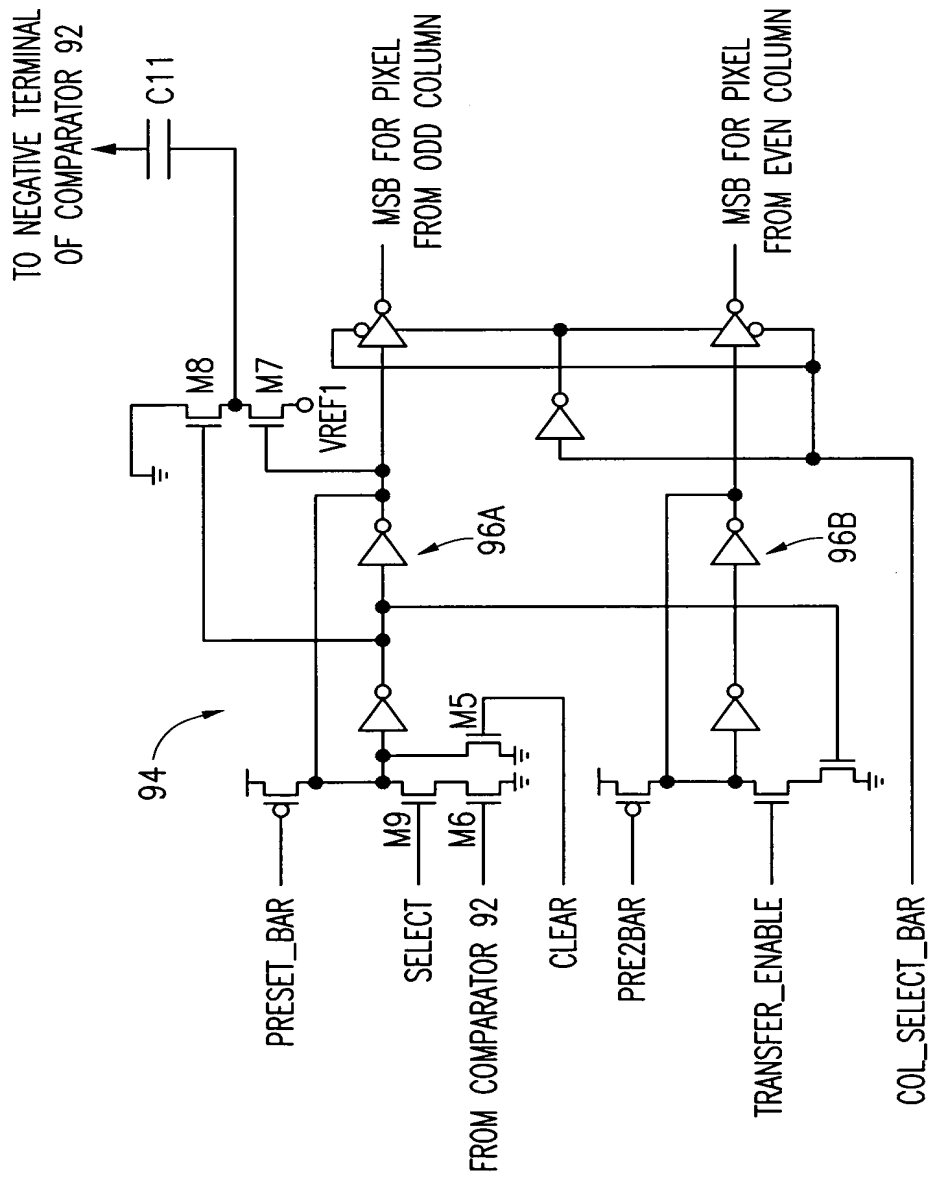
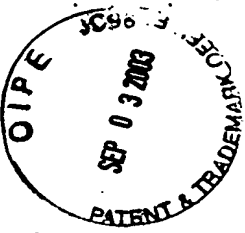


FIG. 10

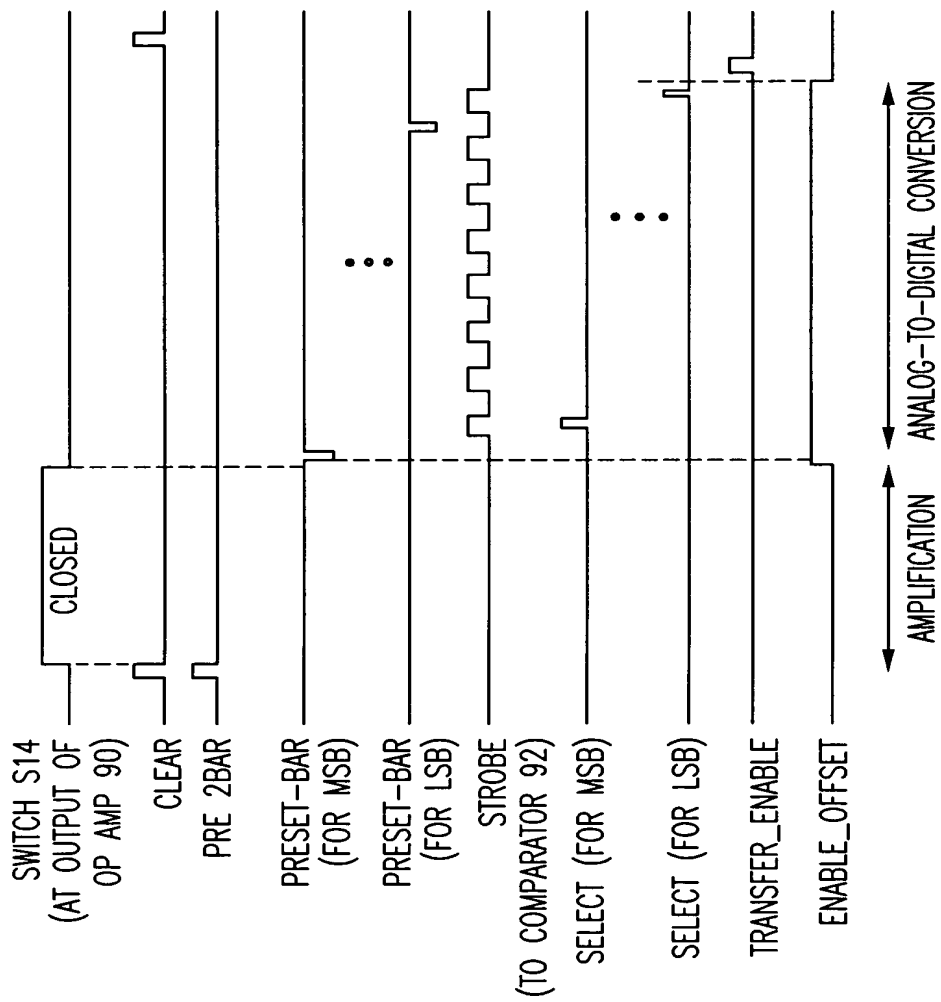


FIG. 11

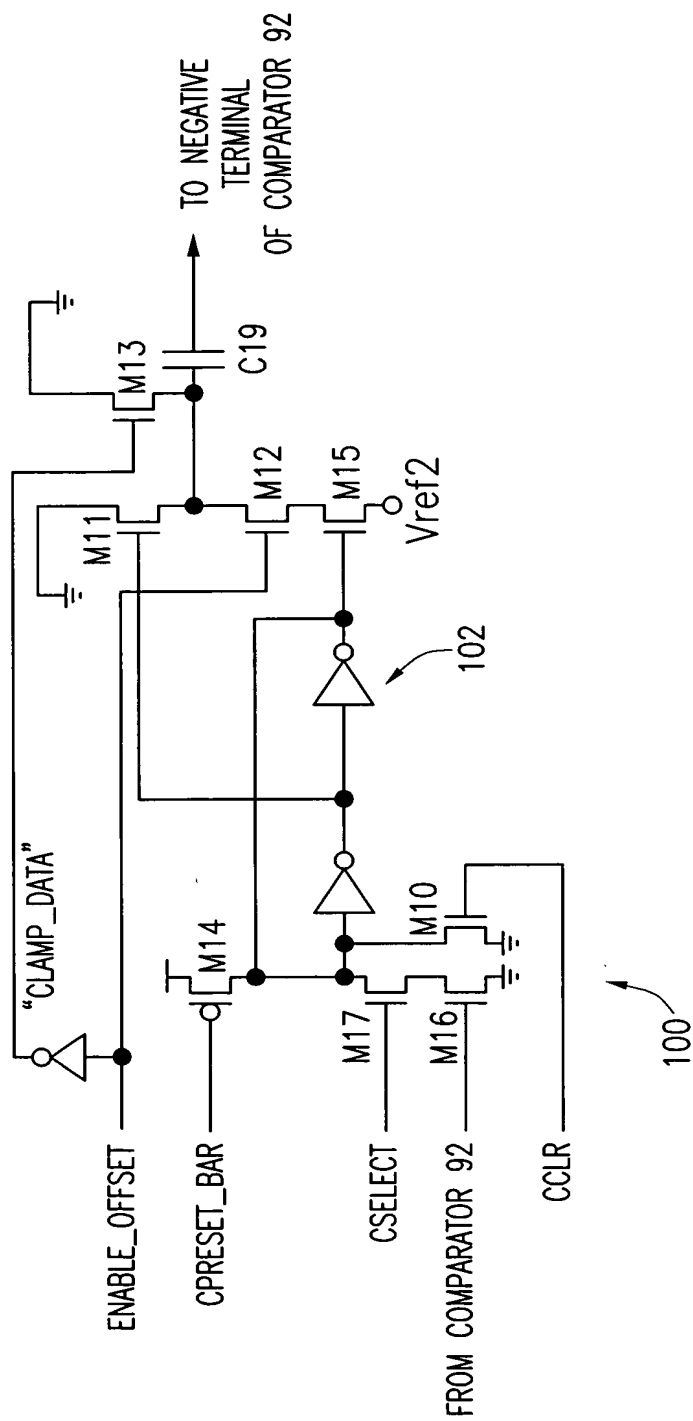
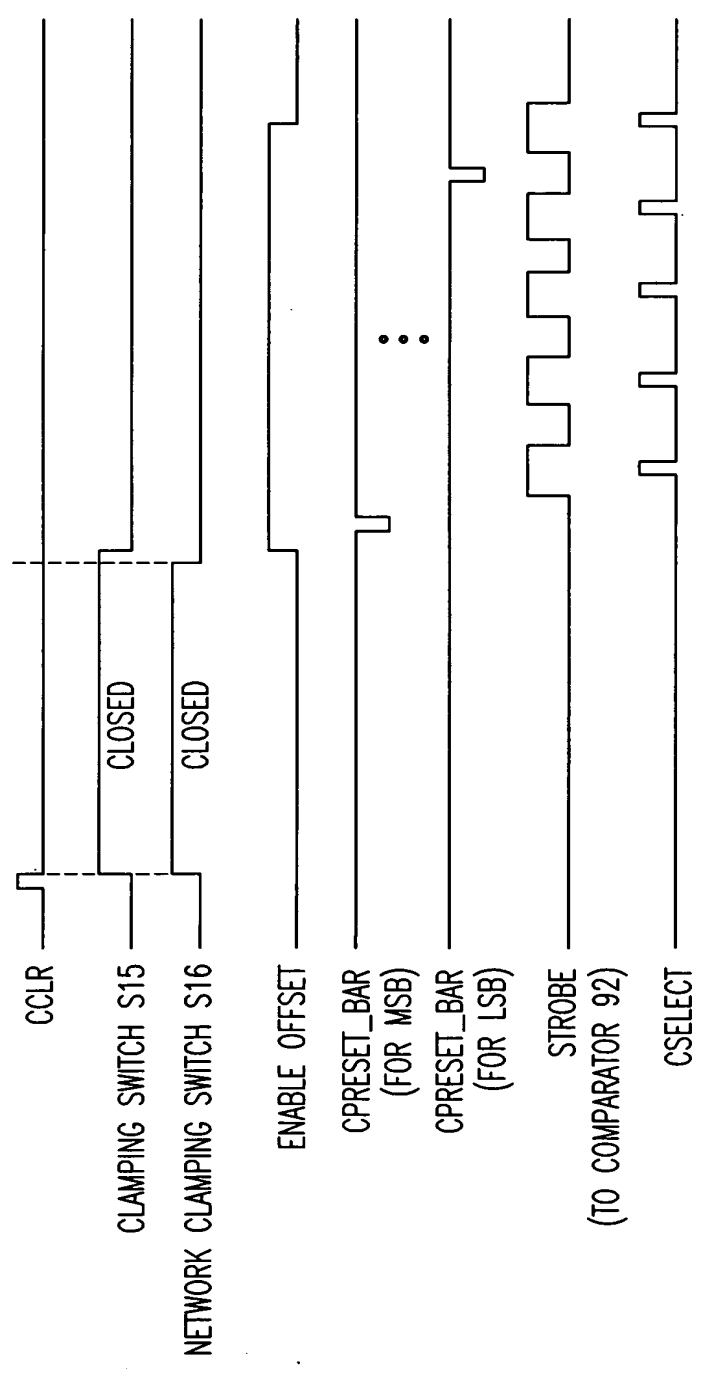
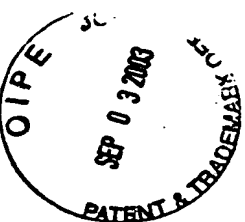
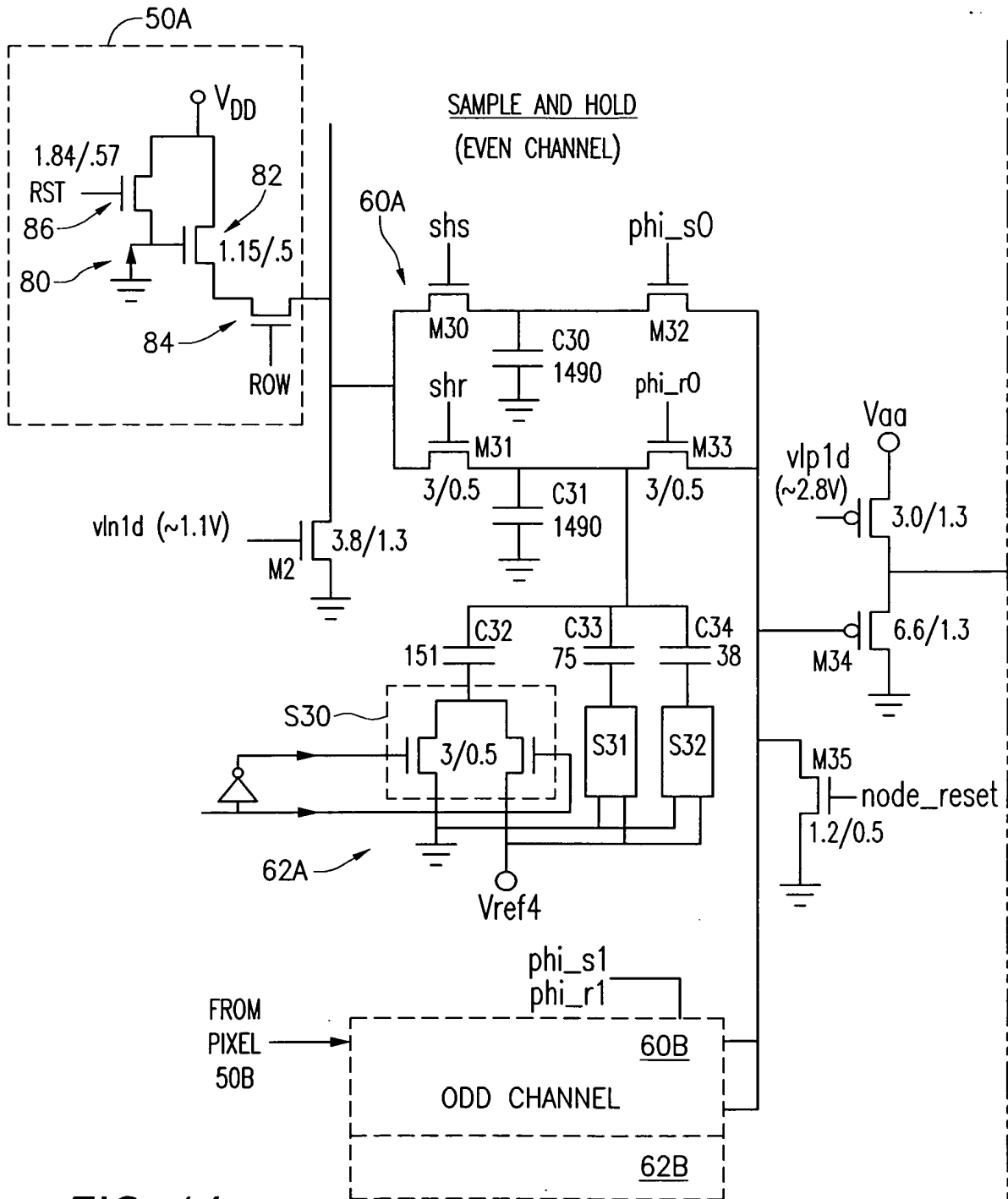


FIG. 12



CALIBRATION TIMING

FIG. 13



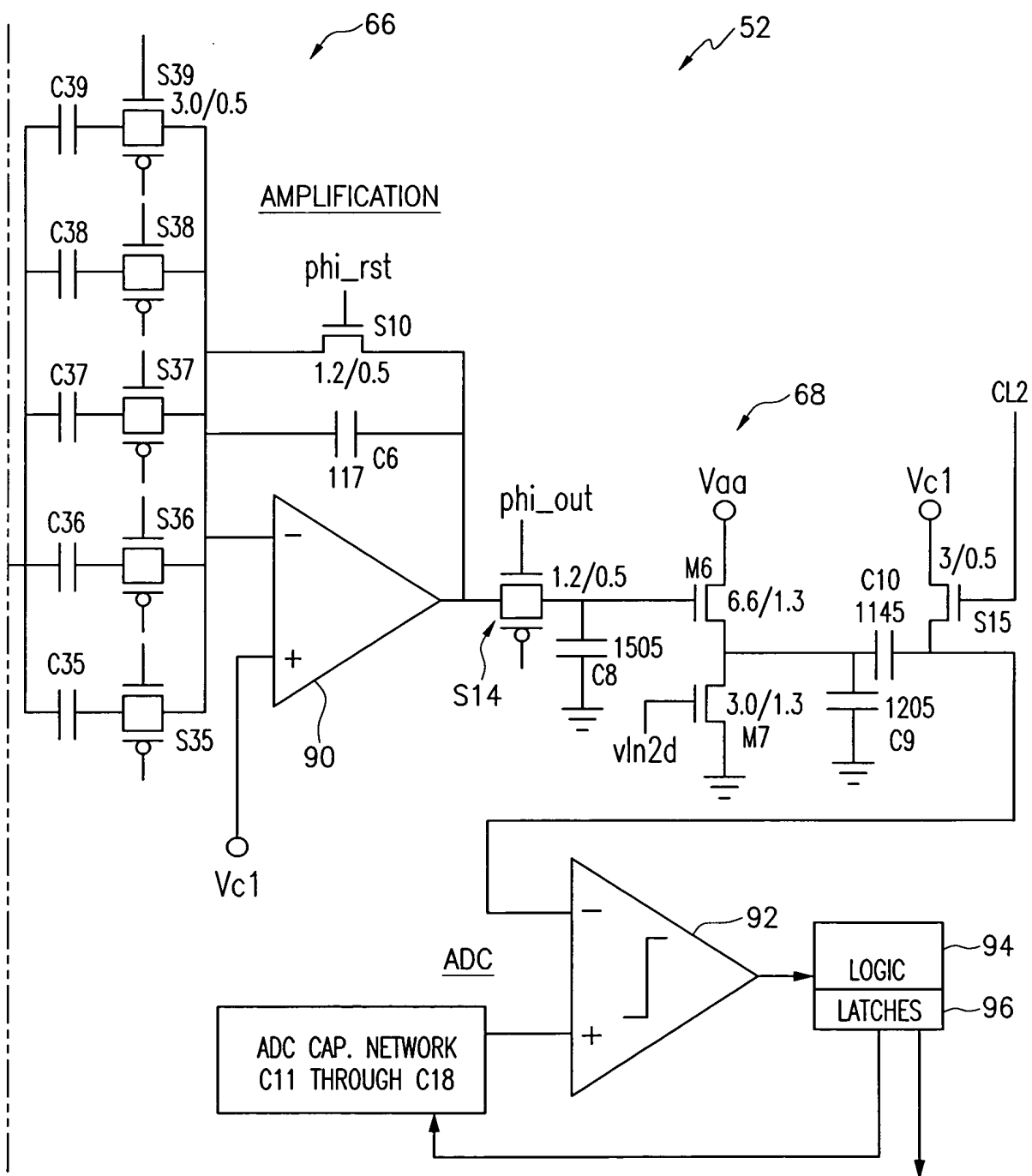


FIG. 14

FIG. 14A

FIG. 14B

FIG. 14B

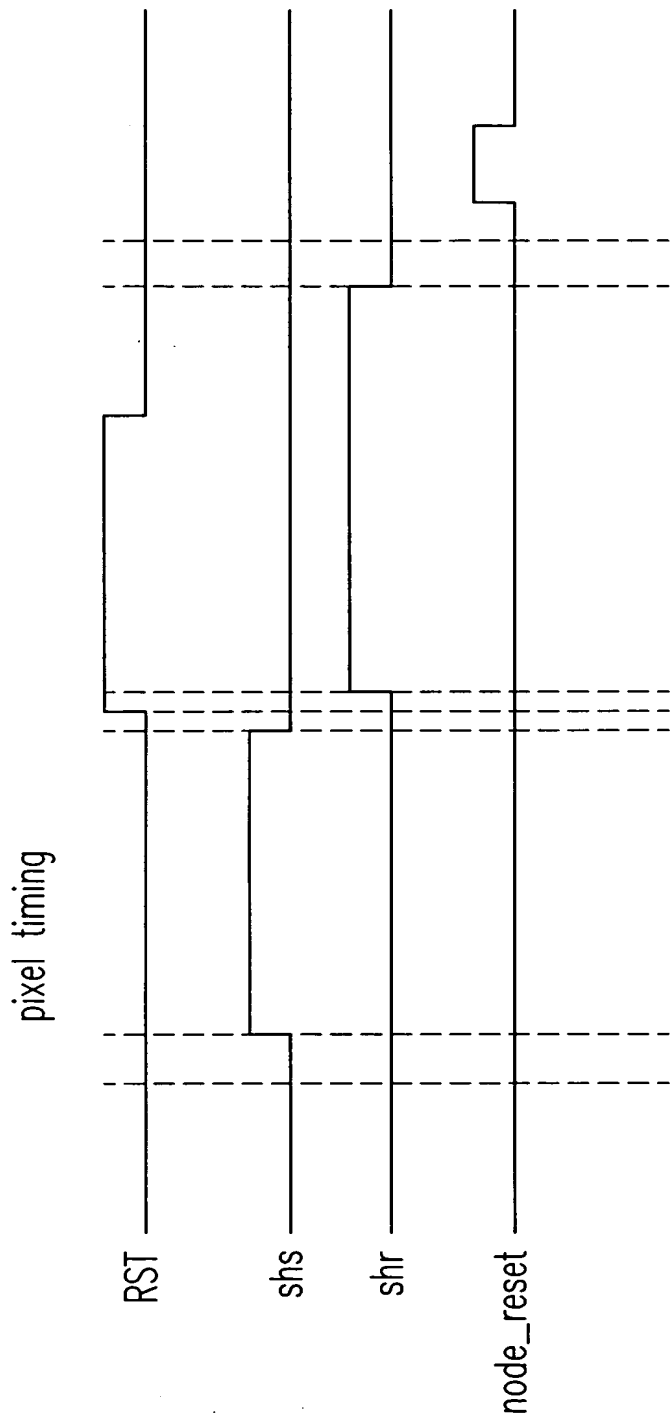
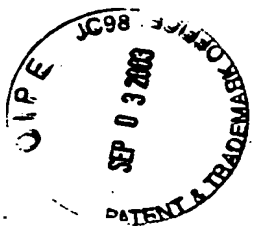


FIG. 15

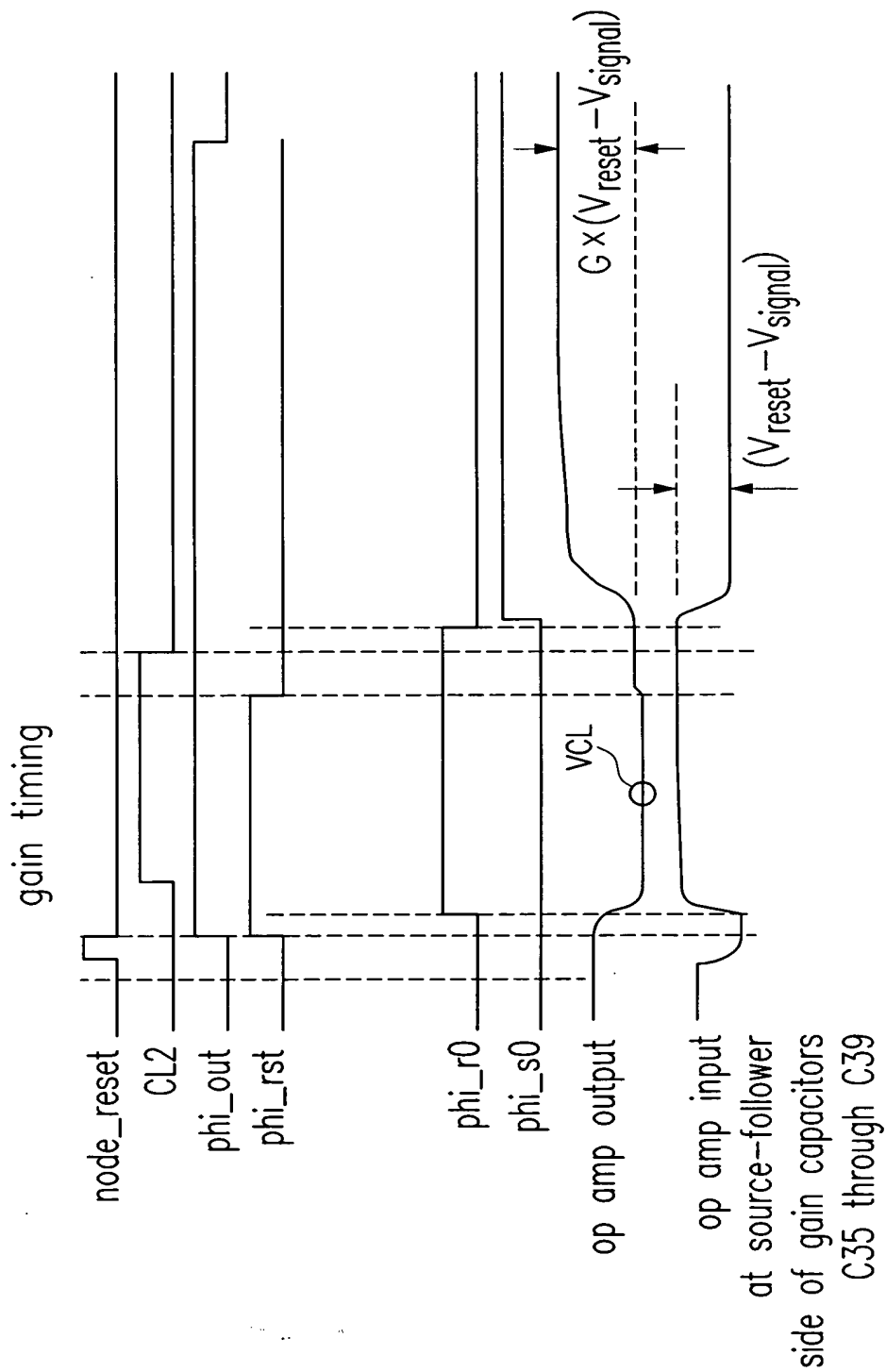


FIG. 16

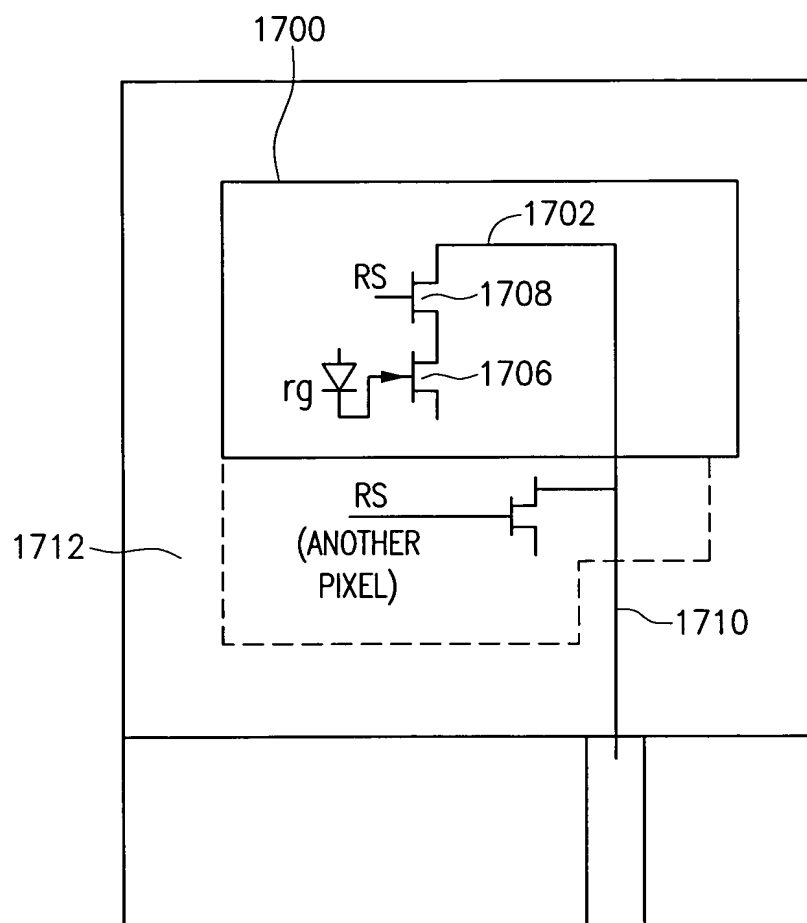
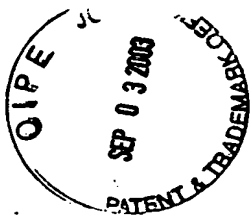


FIG. 17

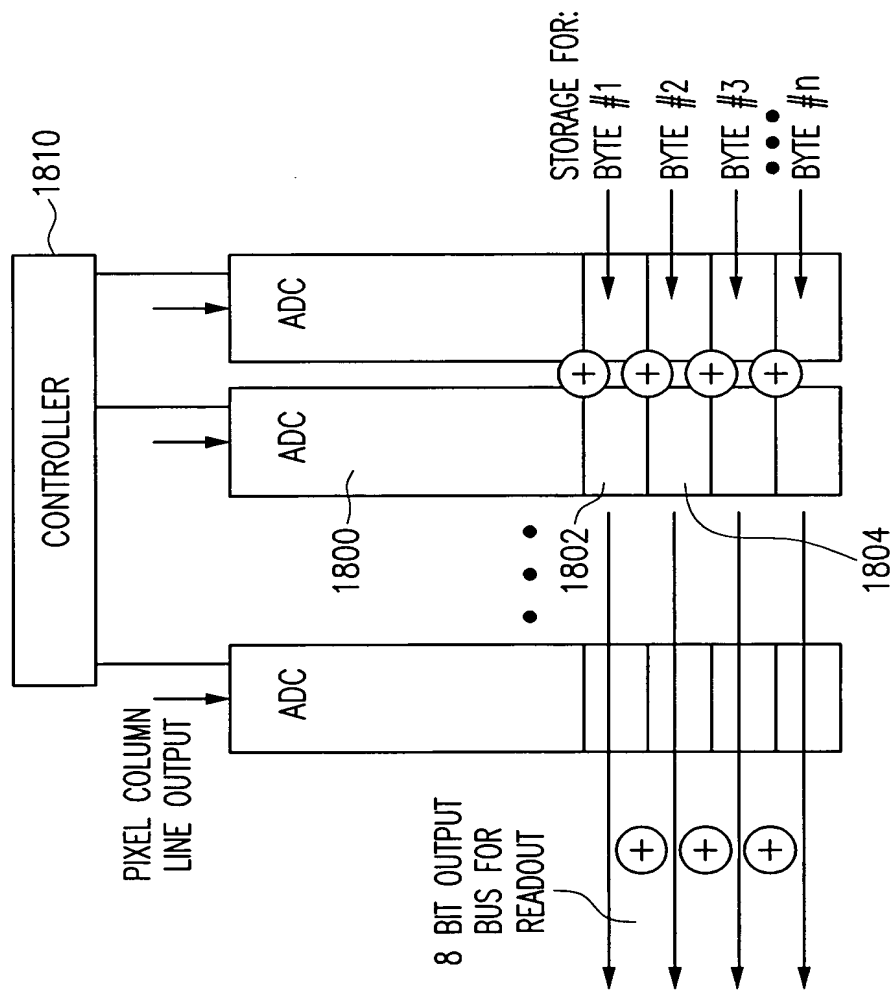


FIG. 18